Abstract

Programmable Field Mounted Device

Sub B1

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To programming the device during running a computational
process in device electronics, the device having a
activated first memory area storing a first programmable
configuration and a deactivated second memory area storing
a second programmable configuration. The the second
programmable configuration is modified. Simultaneously, the
first programmable configuration in the first memory area
is executed by the computational process. Upon completion
of configuring the second memory area, the first memory
area is deactivated and the second memory area is activated
for excecuting by the computational process.